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For Leading ASIC Design Tools and IP Libraries. New Synplify ASIC Product Flow-Tested With Physical Design and Test Tools. SUNNYVALE, Calif., June 4, ...  
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### EDN Access -- 09.29.94 ASIC Test: it's a new ball game

That's the good news about ASIC test. But there is a dirty little secret ... of tools involved in ASIC test is "test-development automation" (TDA) tools. ...  
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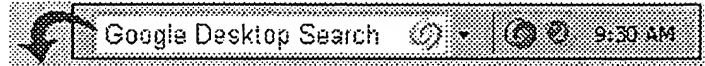
### FPGA Prototyping Tool Aligns With ASIC Flow

... it easier for designers to test out a complex ASIC design on a single FPGA.

... Specifically, DC FPGA works well with the **ASIC** flow by accepting the ...  
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